

FIG. 1A

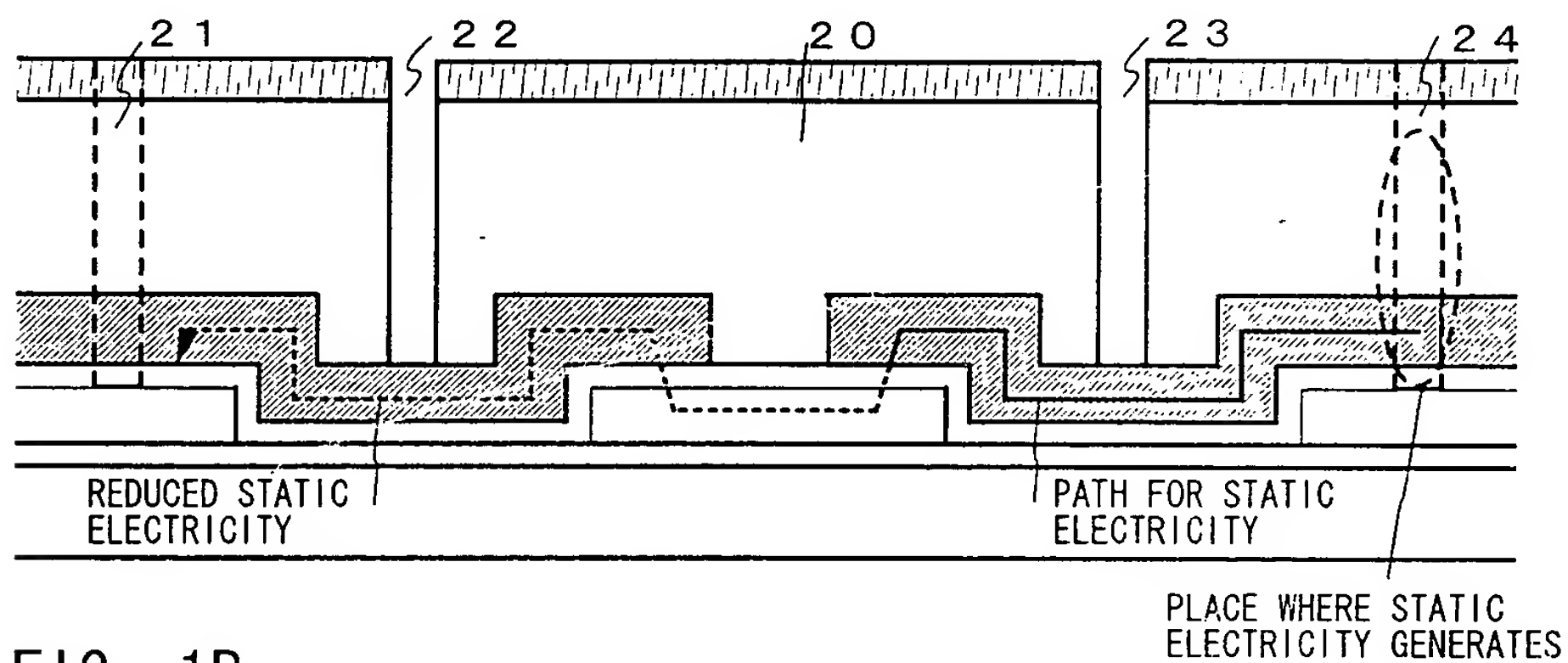


FIG. 1B

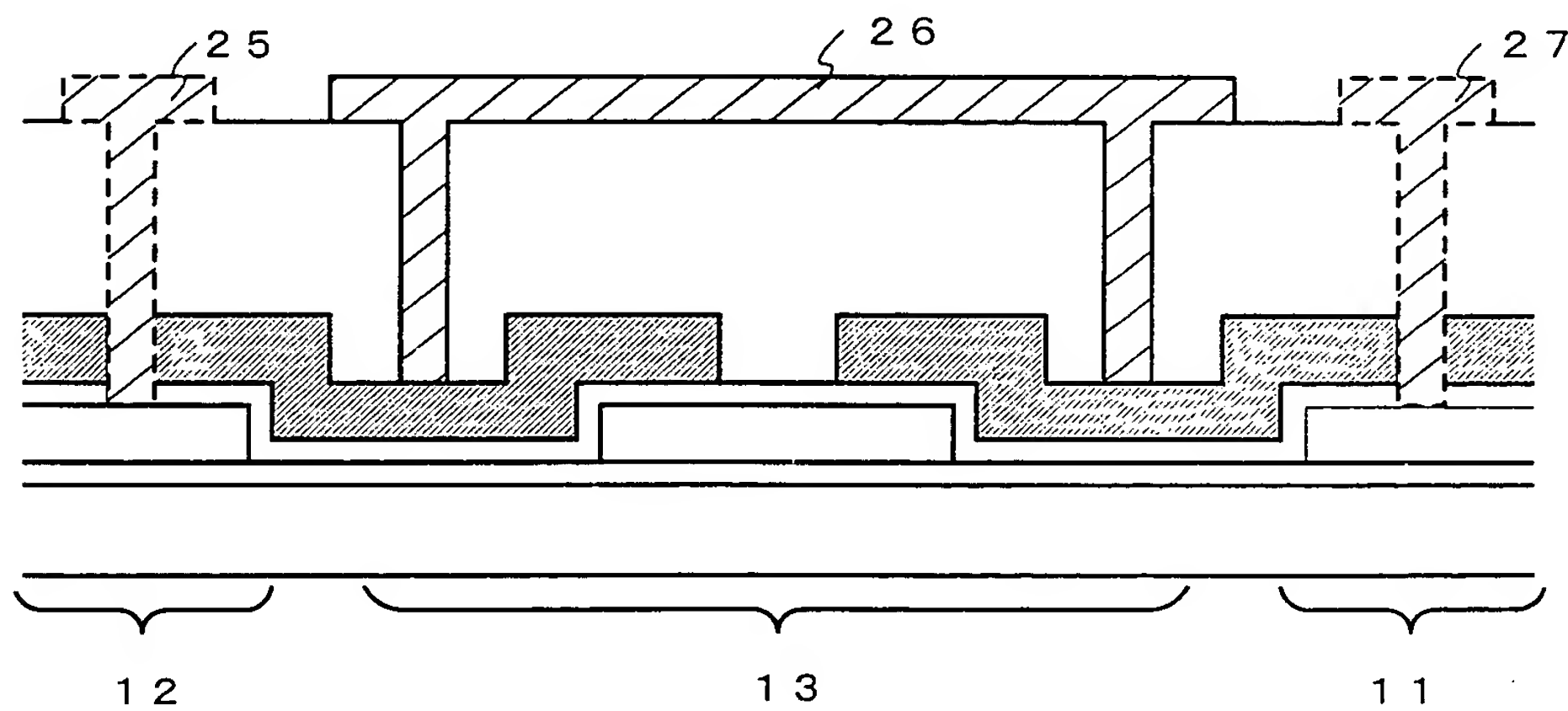


FIG. 1C

FIG. 1A

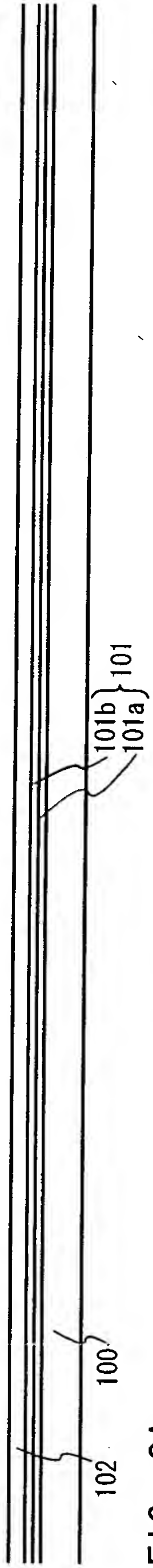


FIG. 2A

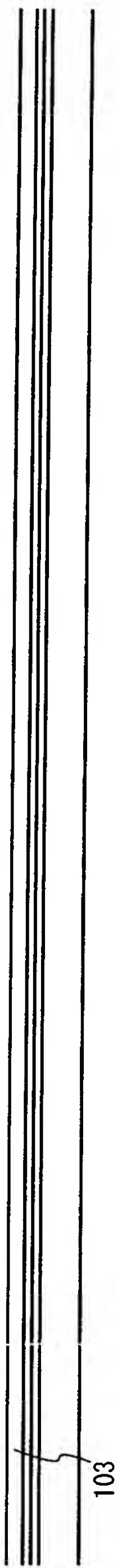


FIG. 2B



FIG. 2C



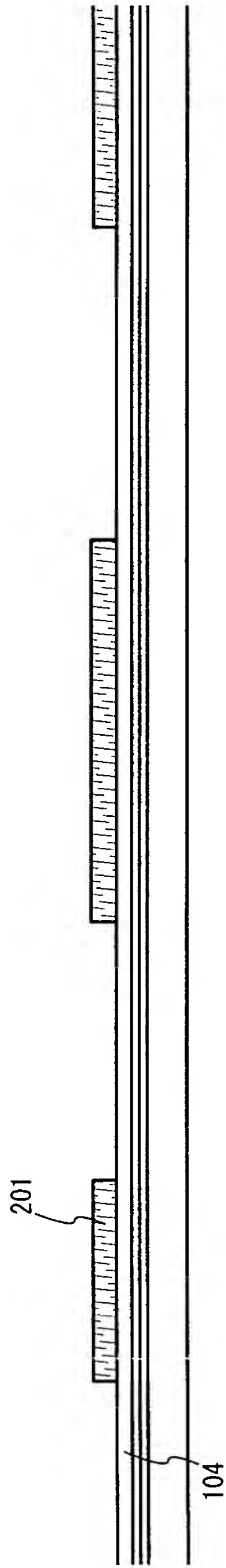


FIG. 3A

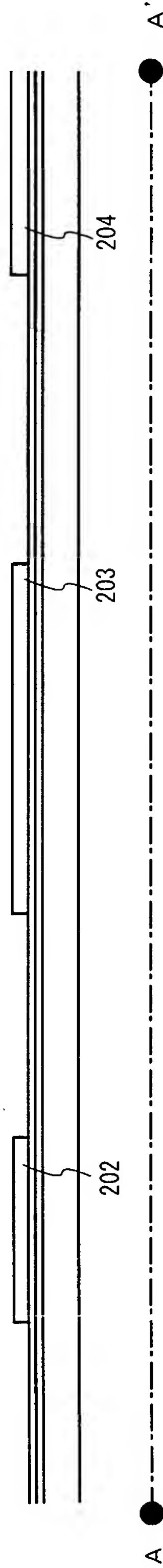


FIG. 3B

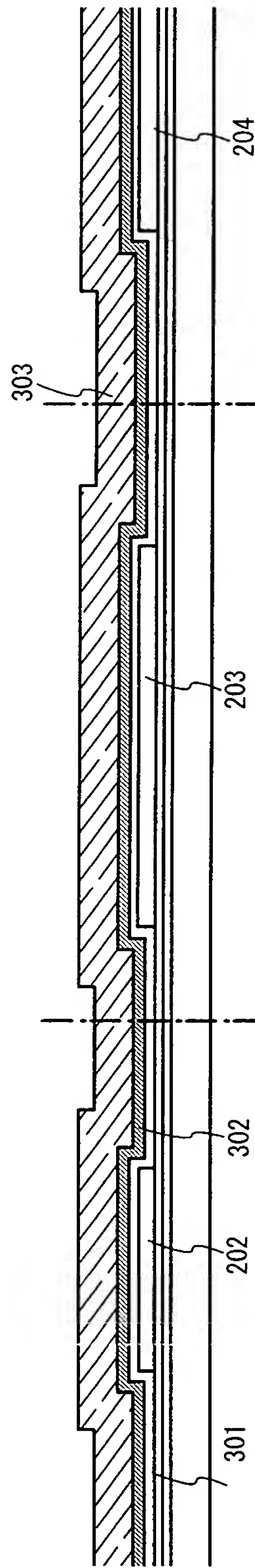


FIG. 4A

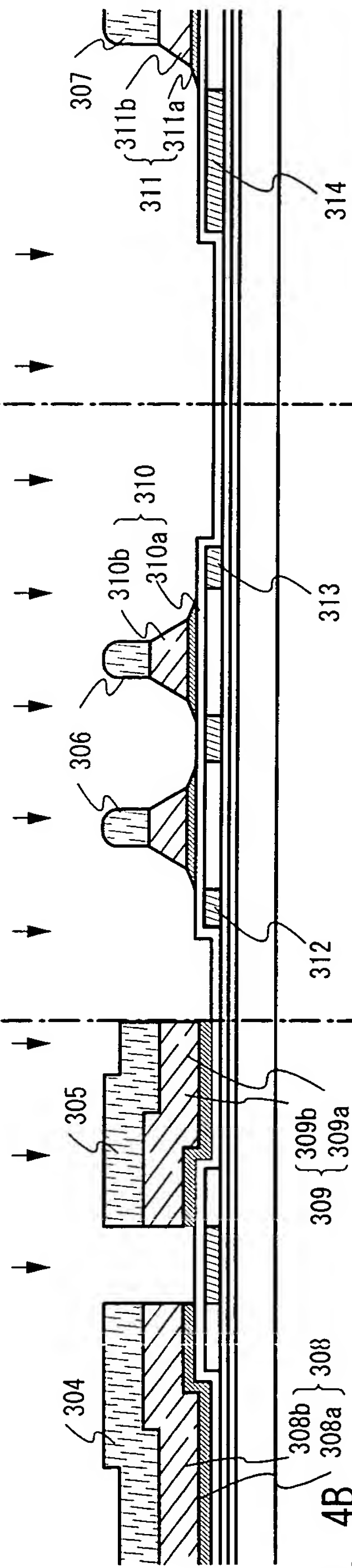


FIG. 4B

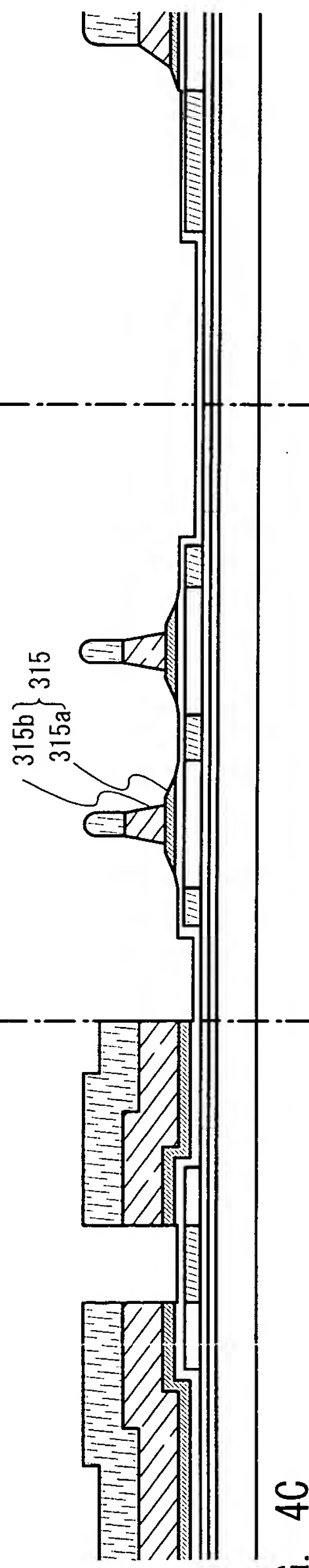


FIG. 4C

A ●-----● A'

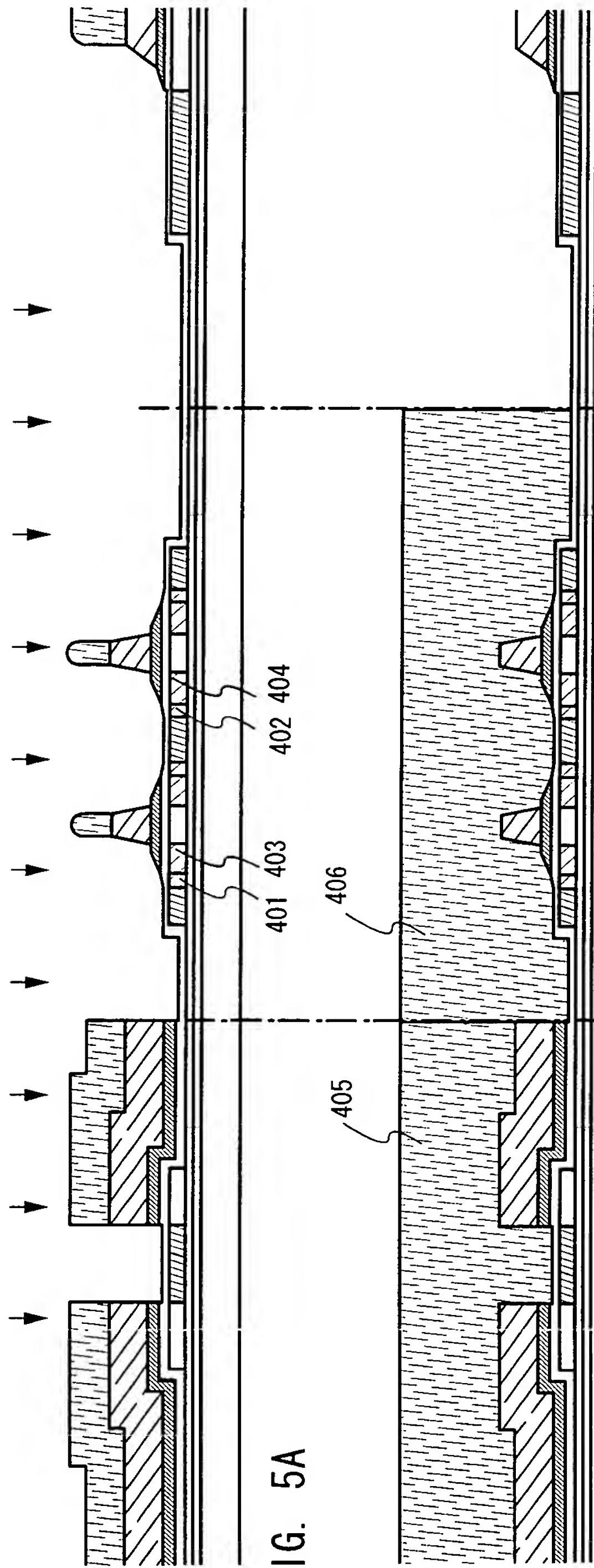


FIG. 5A

FIG. 5B

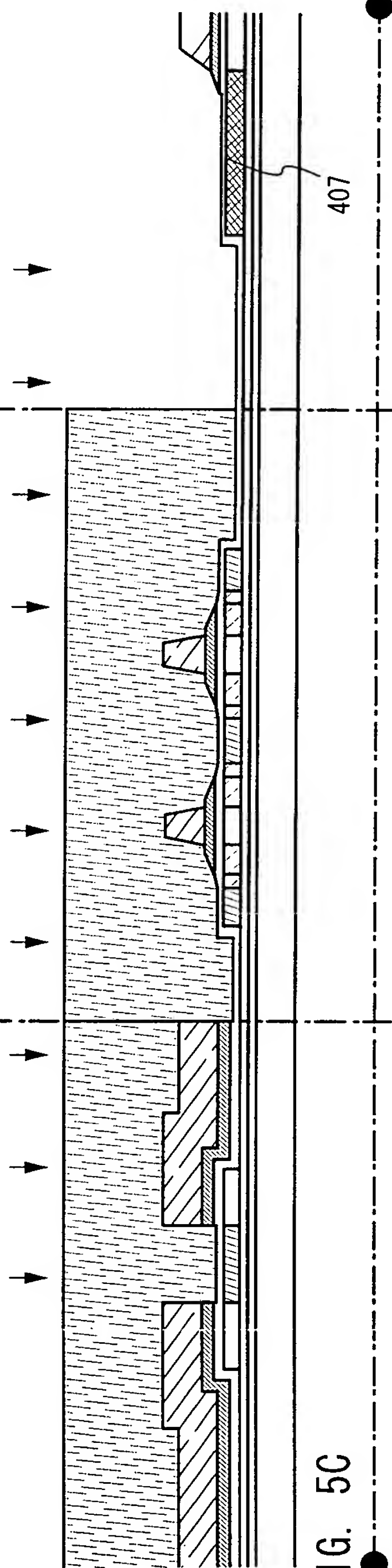


FIG. 5C

A ●-----● A'

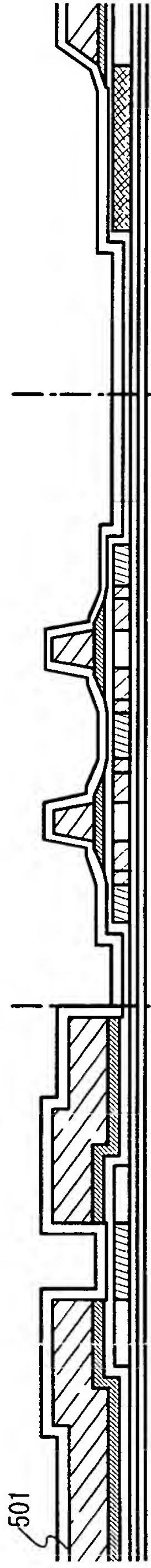


FIG. 6A

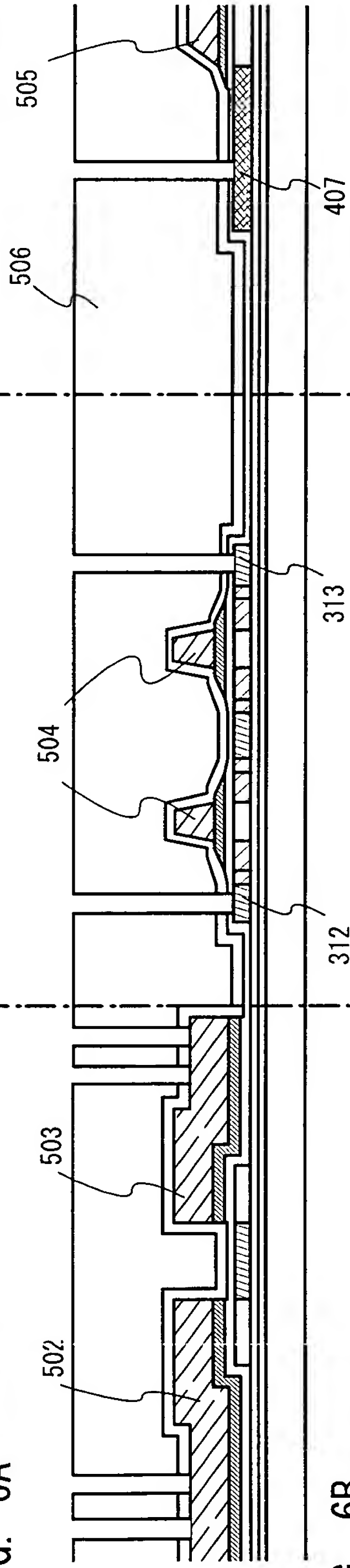


FIG. 6B

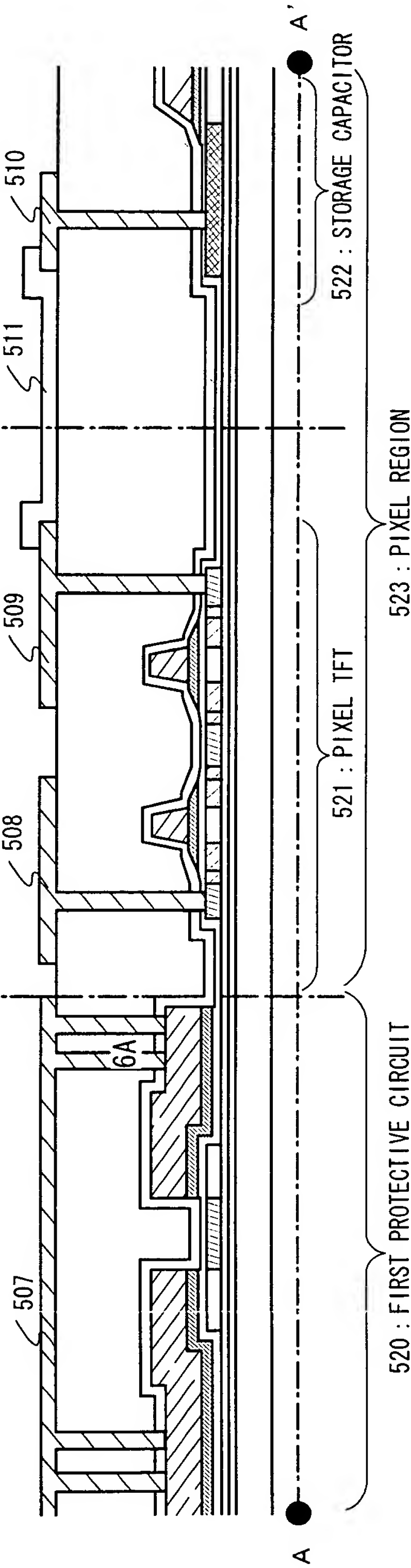


FIG. 6C

A A' 520 : FIRST PROTECTIVE CIRCUIT 521 : PIXEL TFT 522 : STORAGE CAPACITOR 523 : PIXEL REGION

FIG. 7

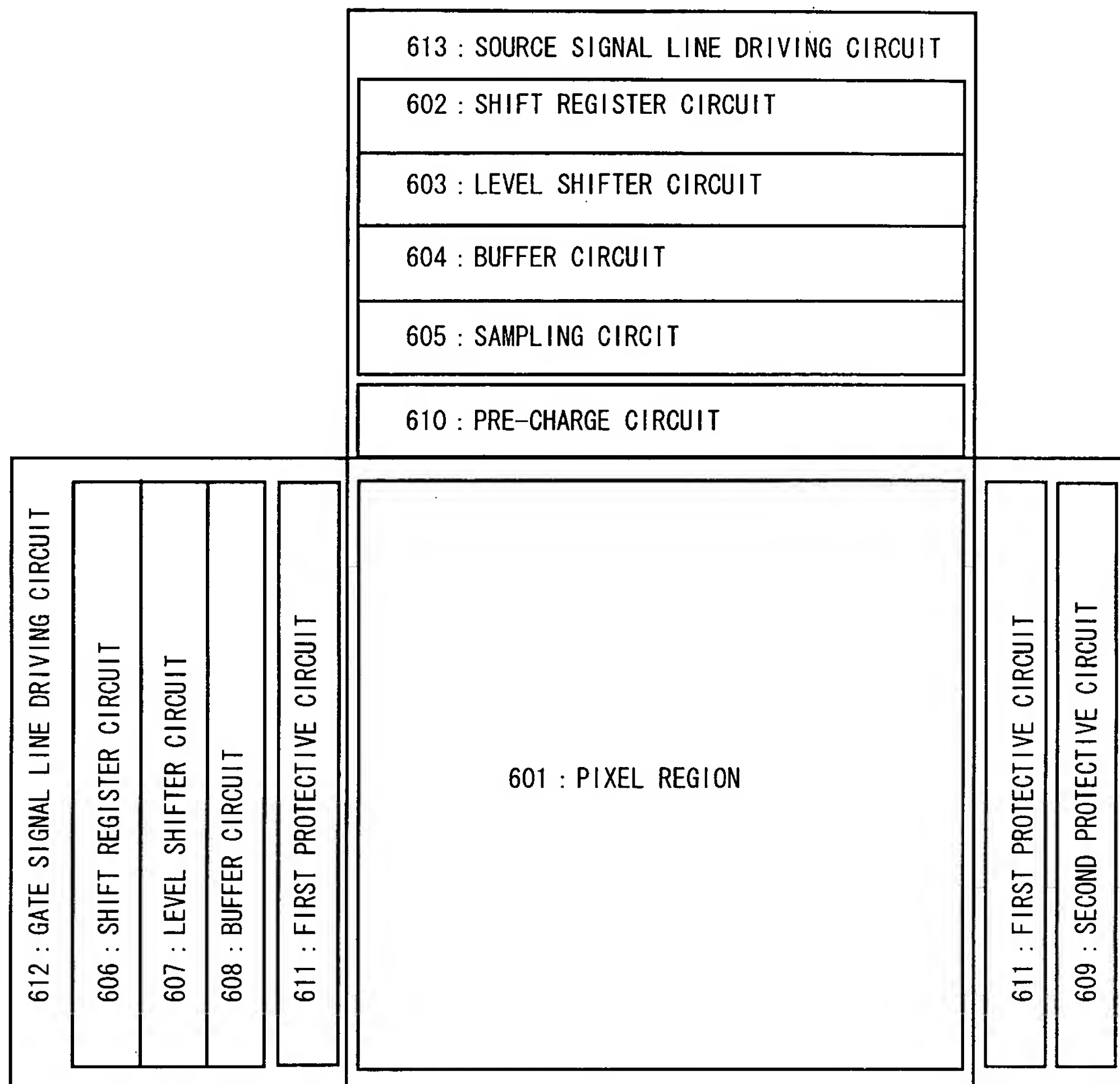


FIG. 7

FIG. 8A

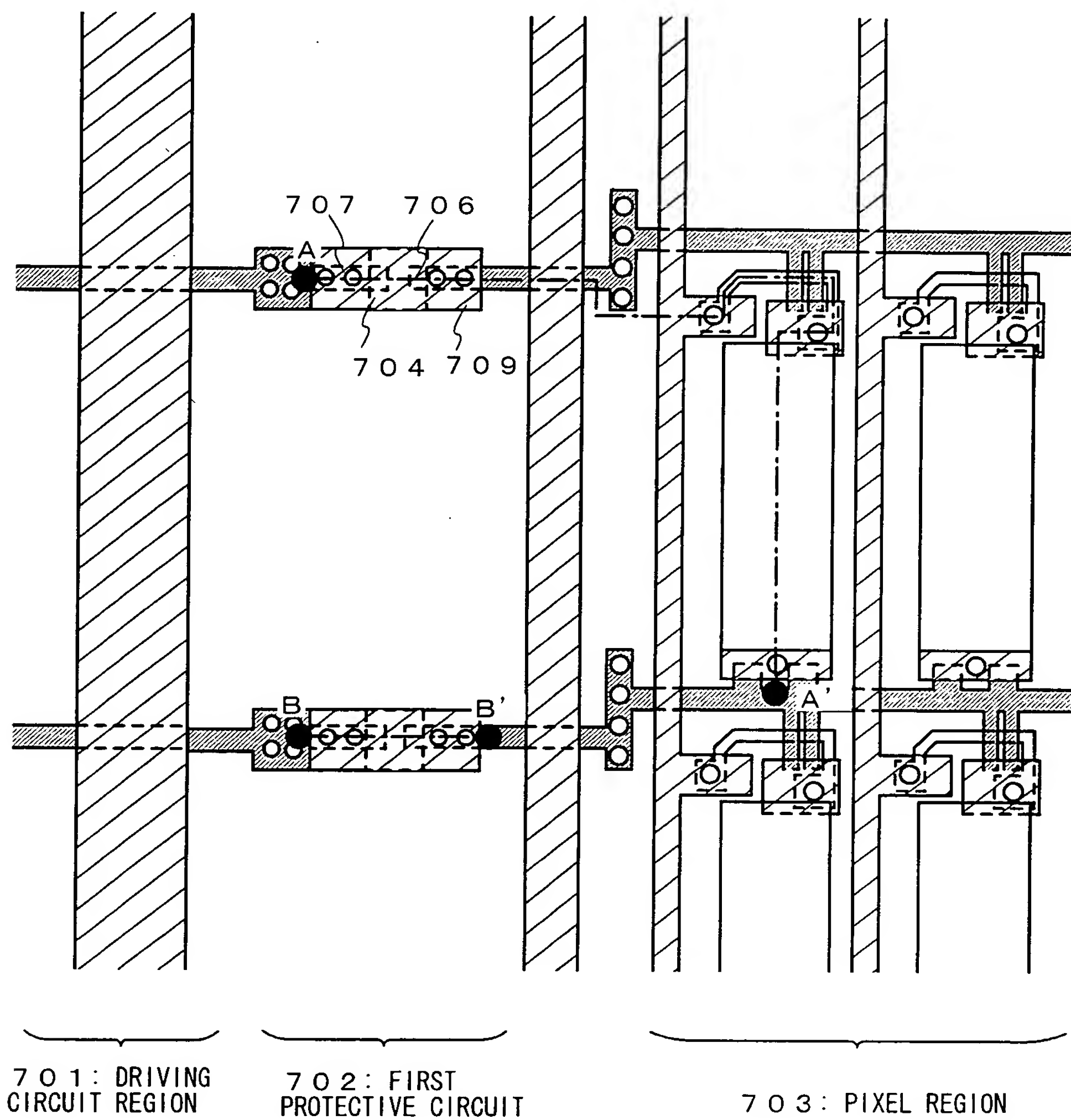


FIG. 8A

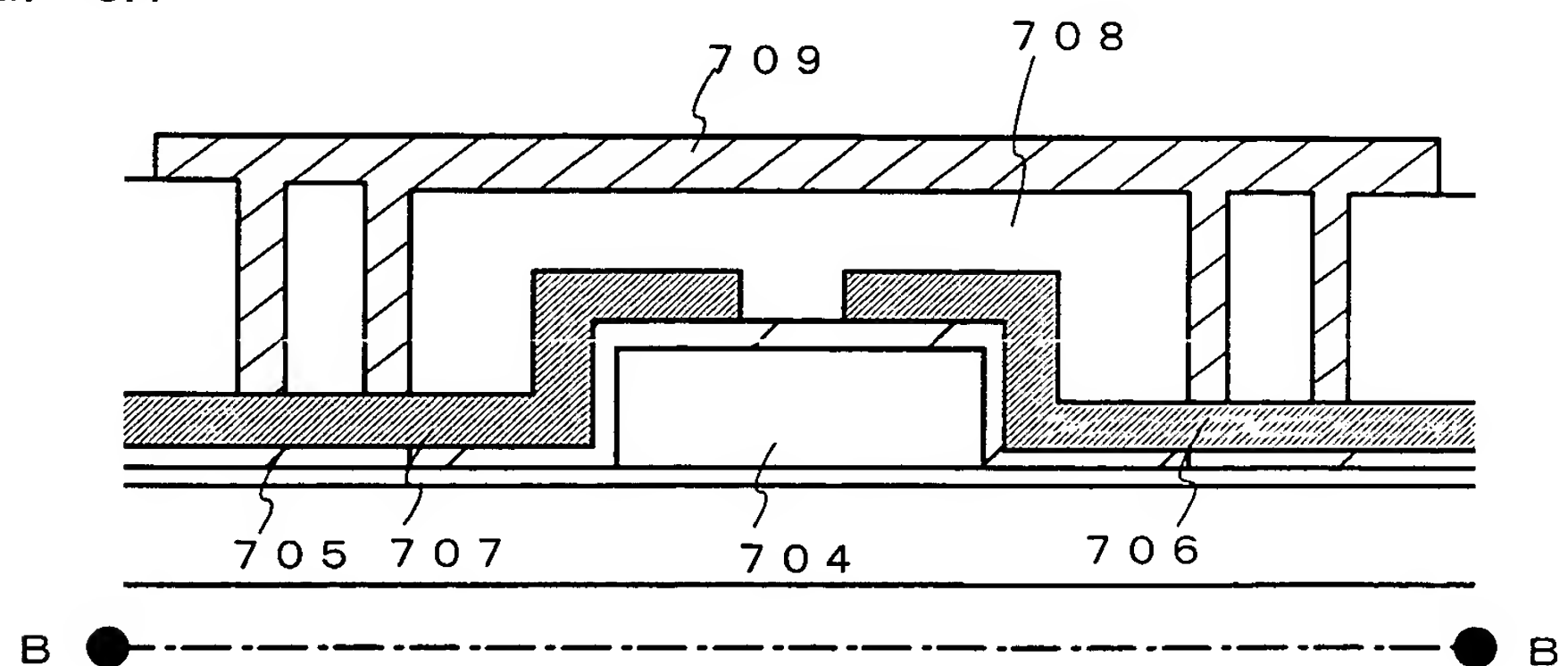


FIG. 8B

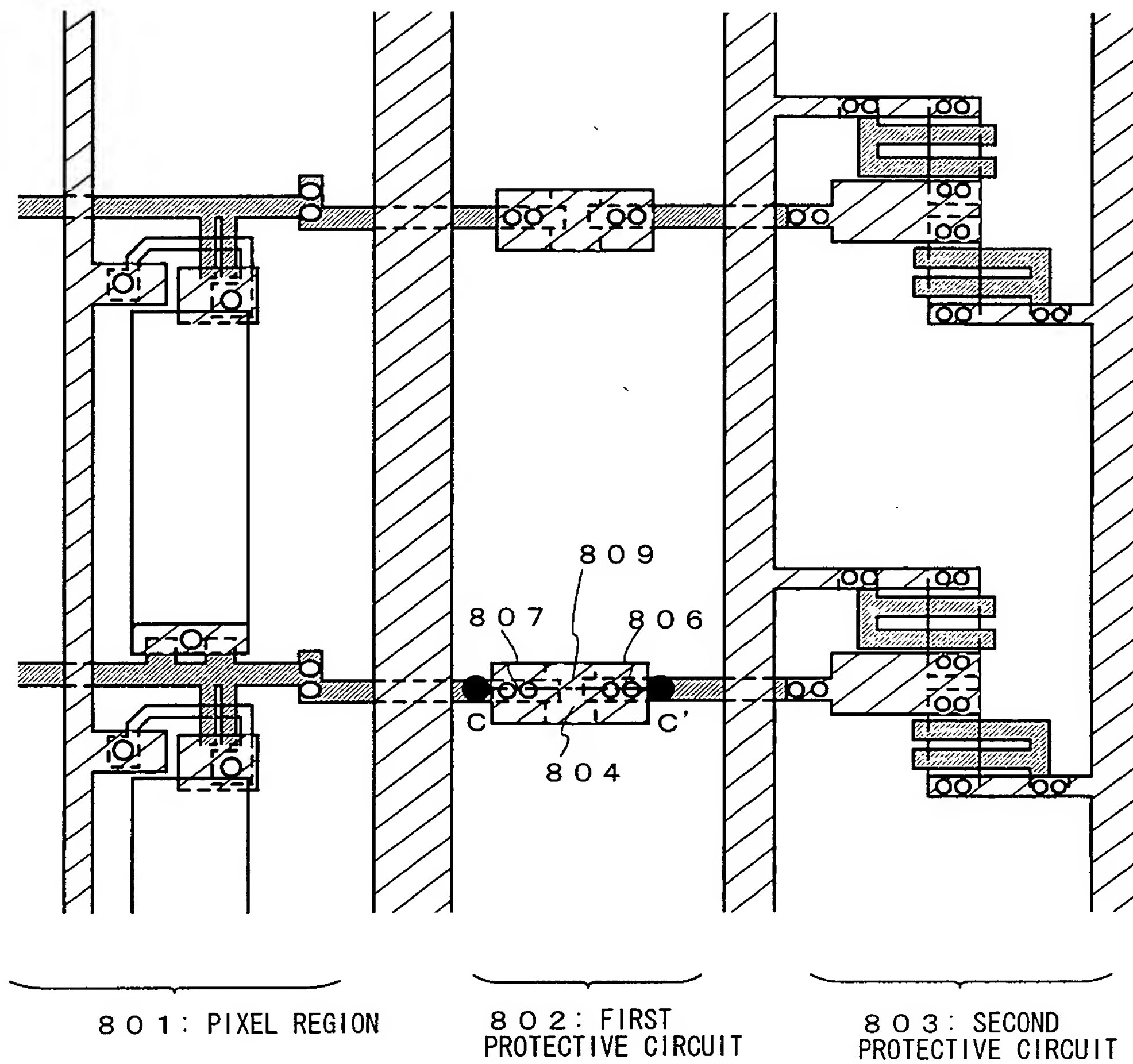


FIG. 9A

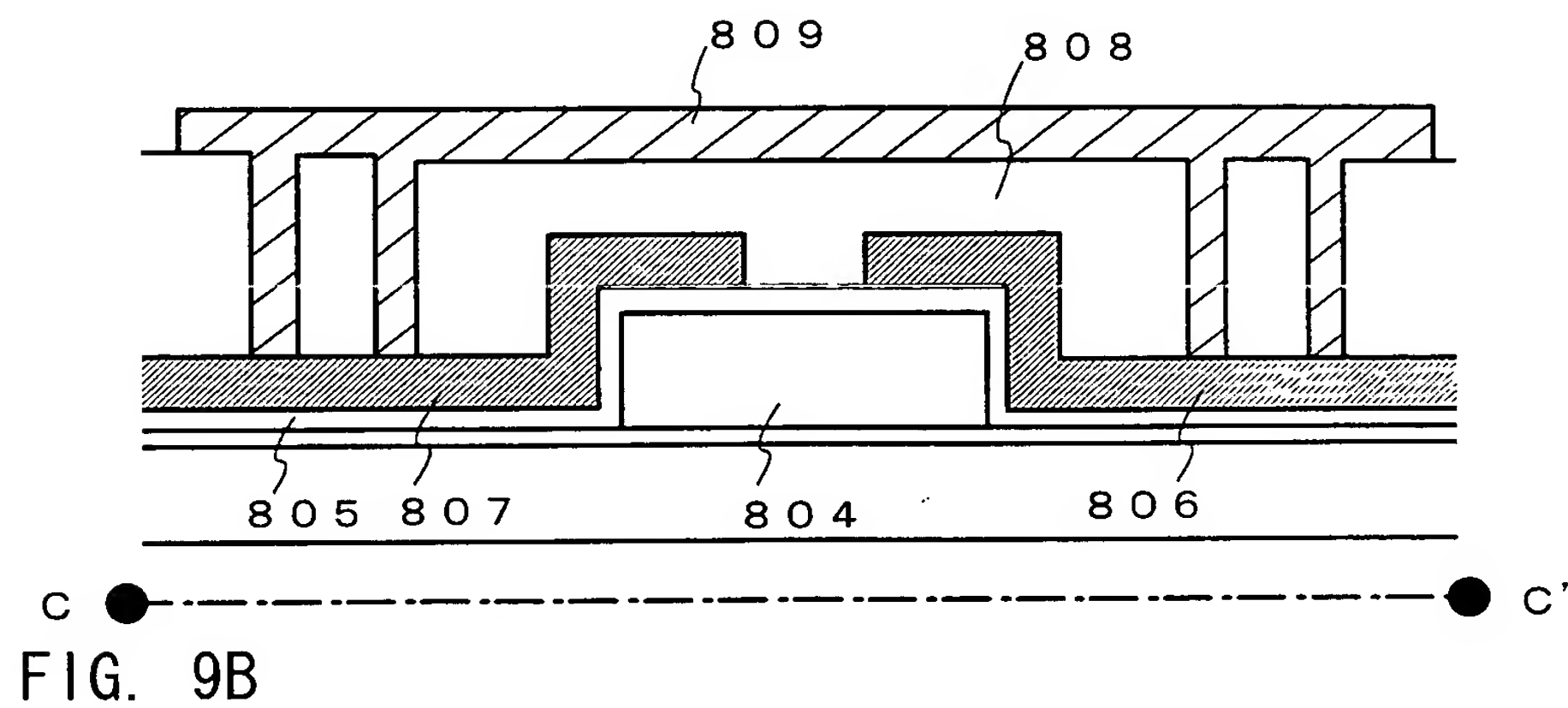


FIG. 9B

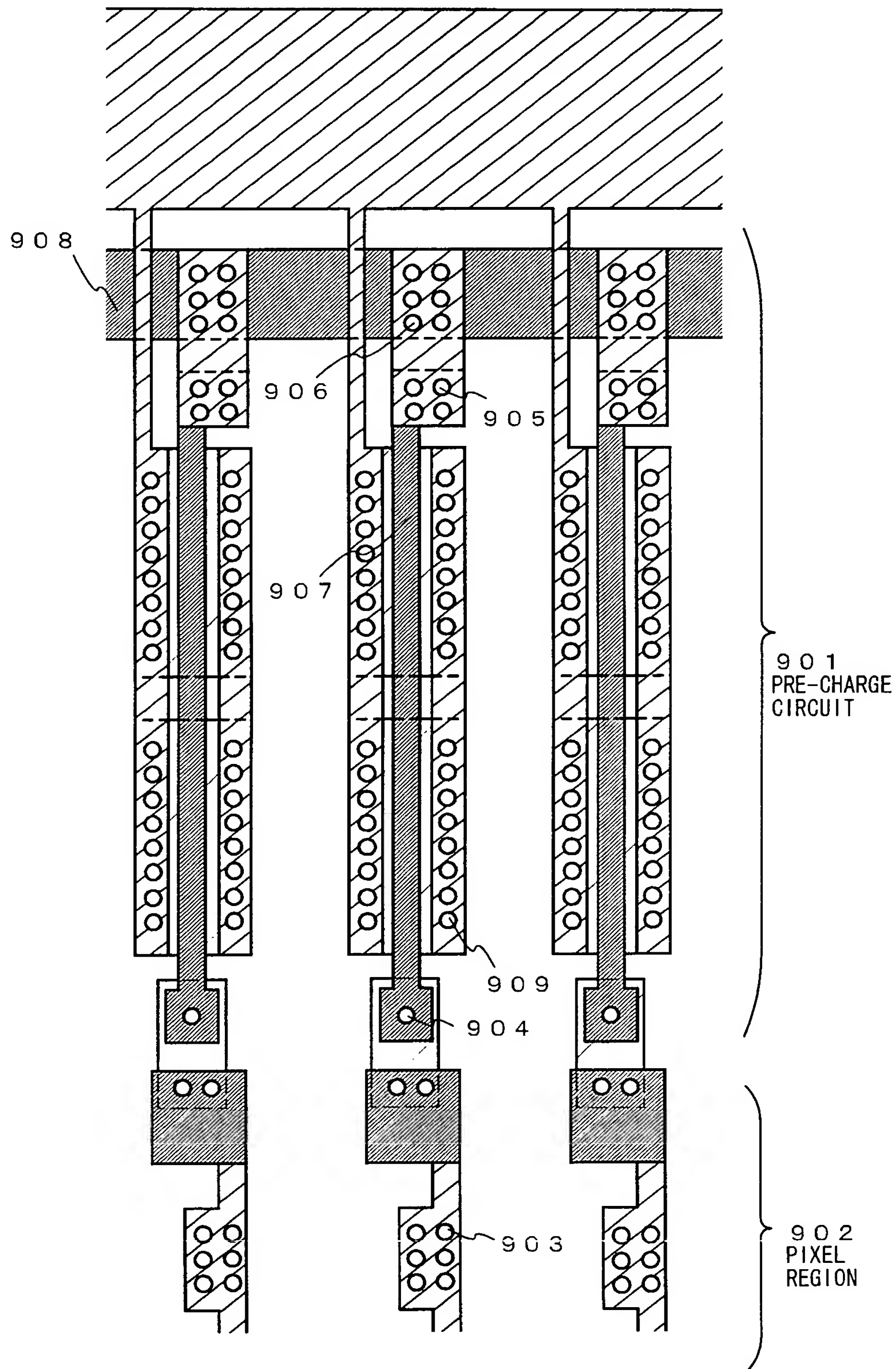


FIG. 10

FIG. 11

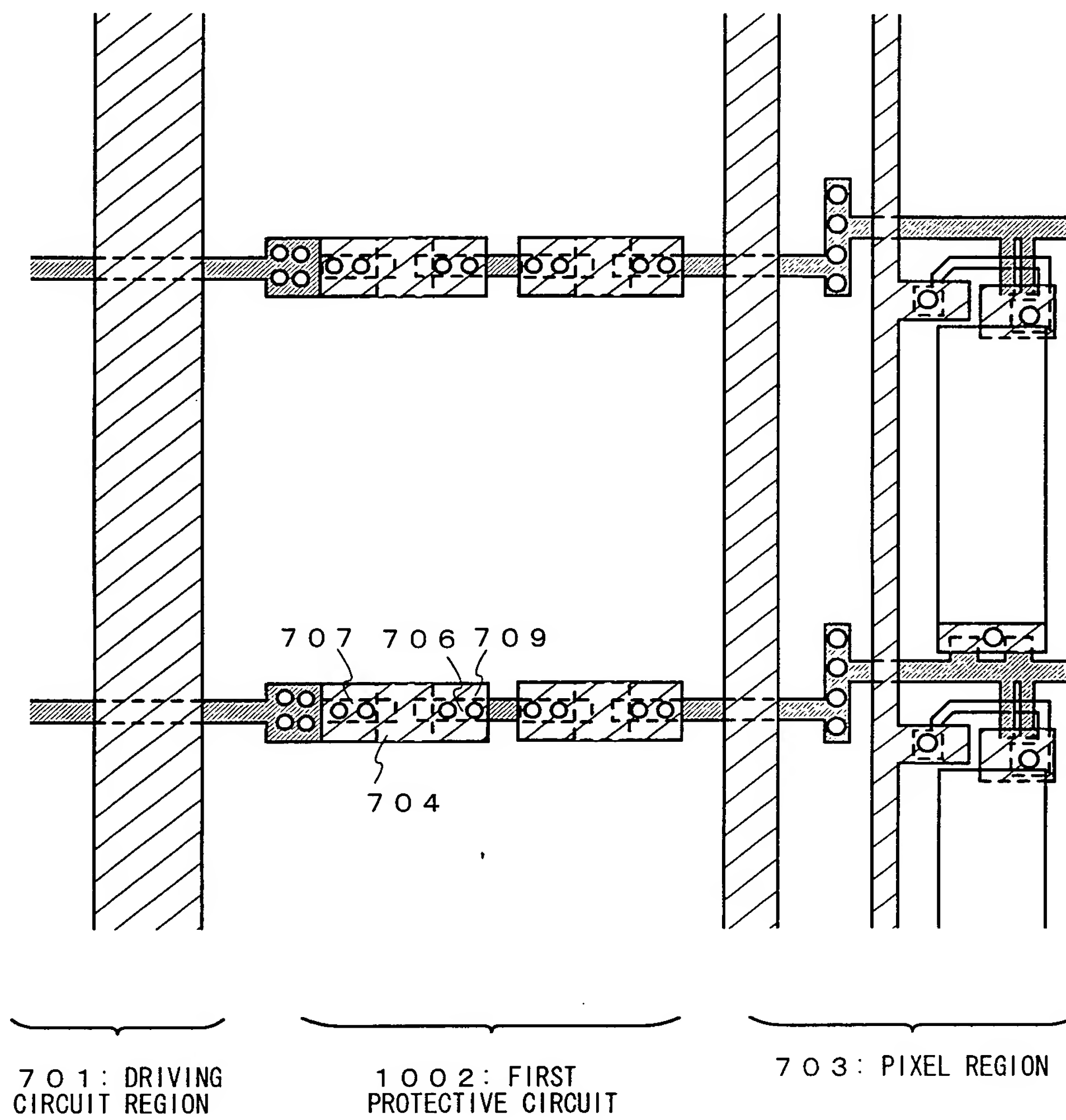


FIG. 11

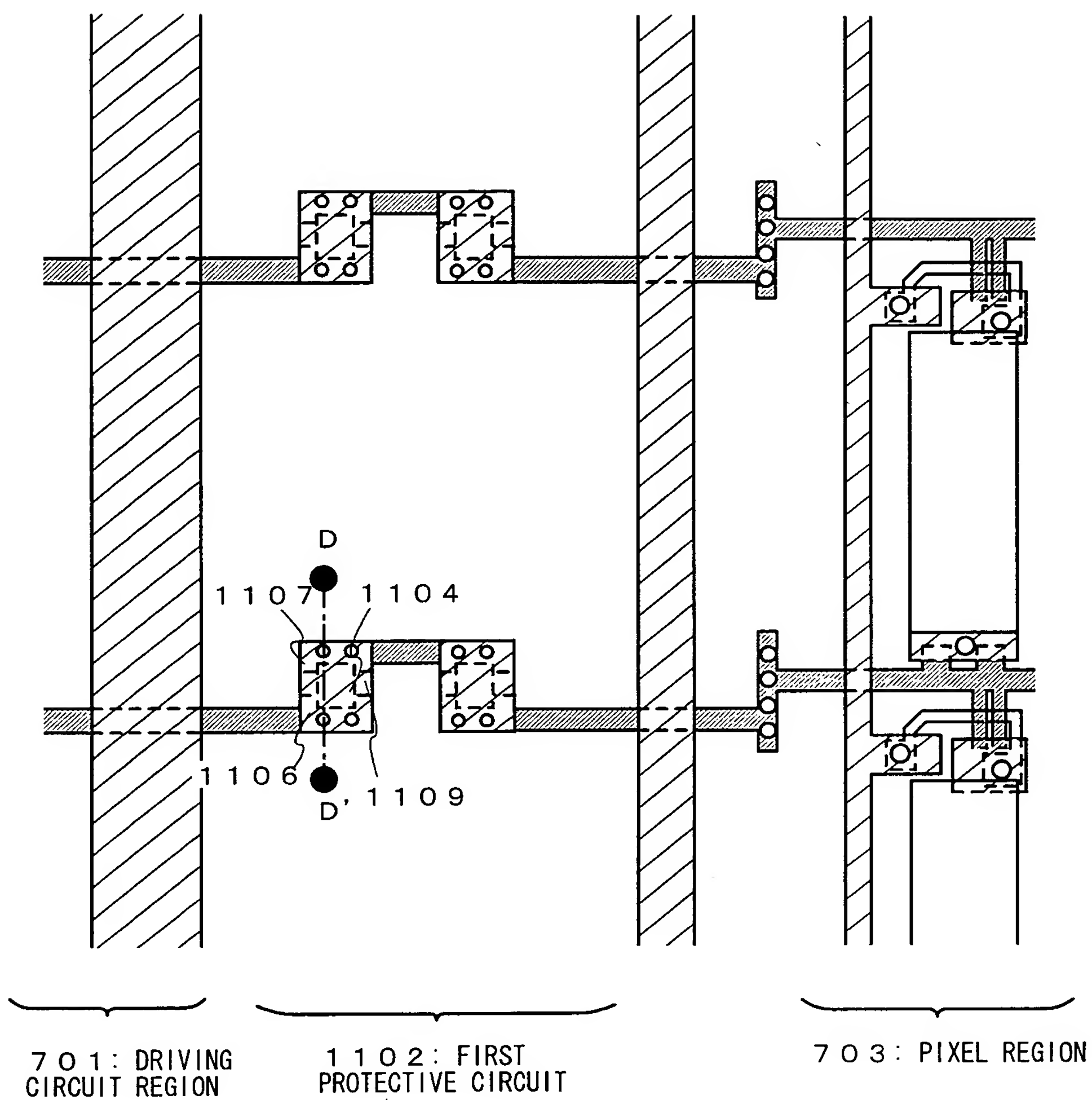


FIG. 12A

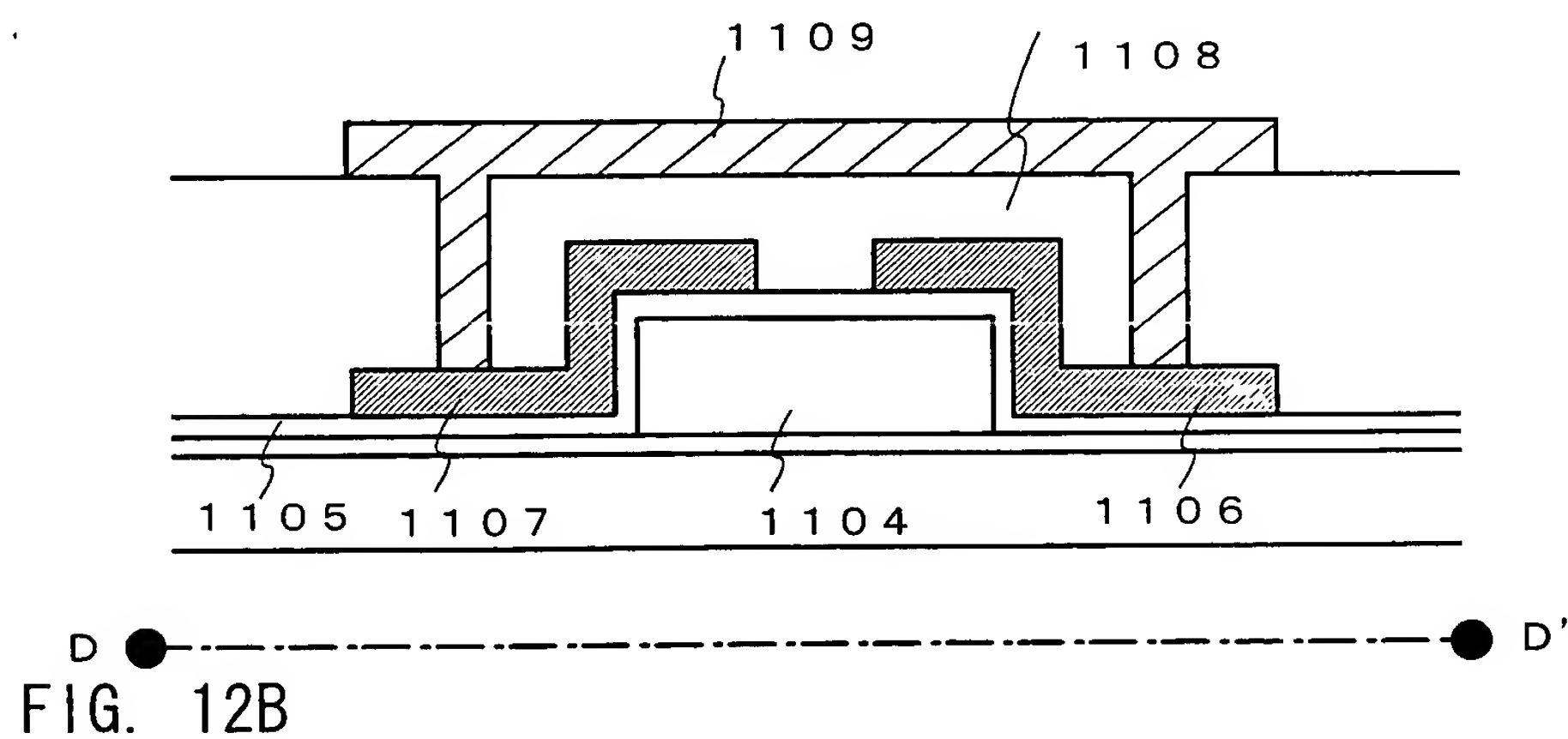
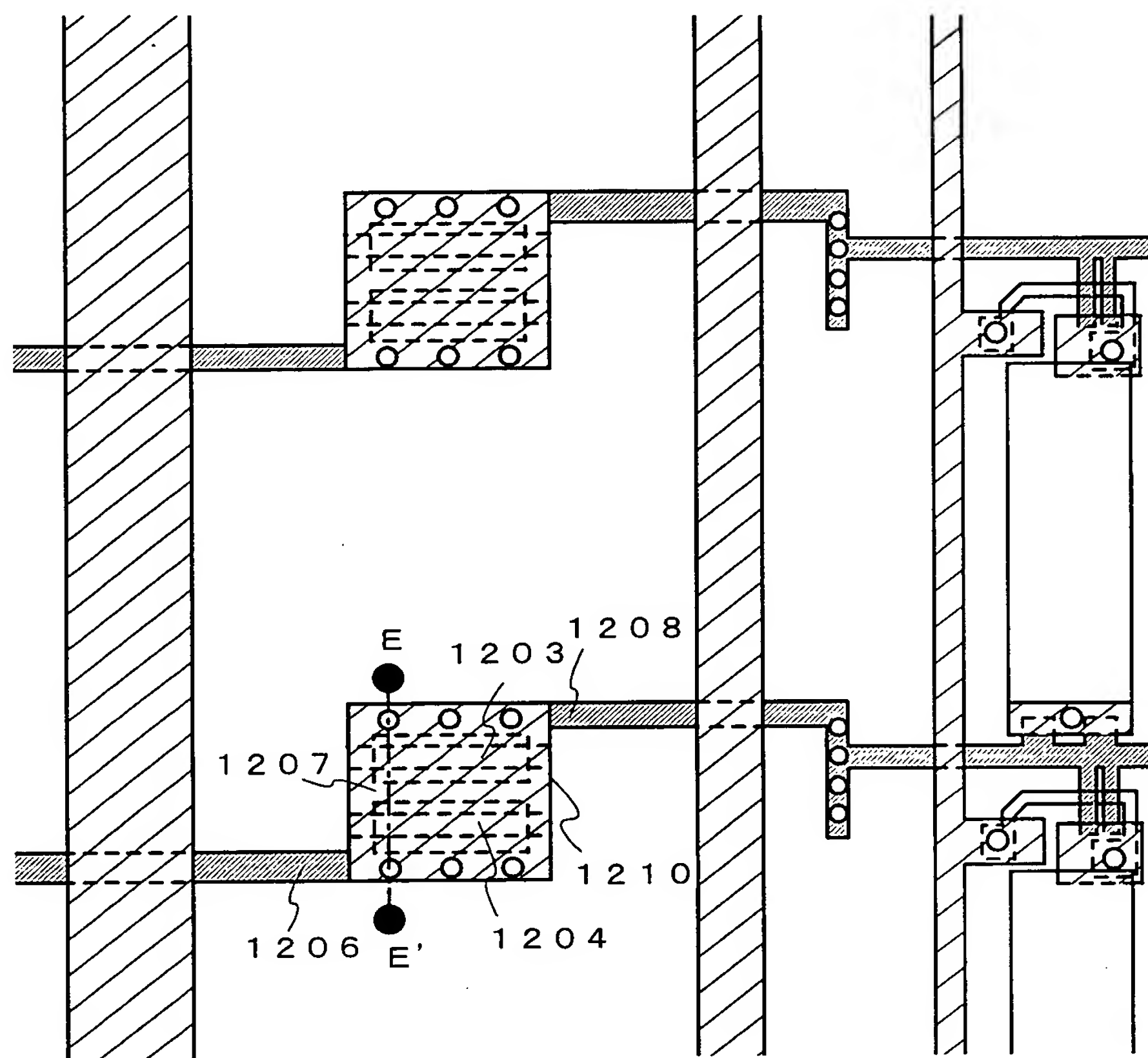


FIG. 12B

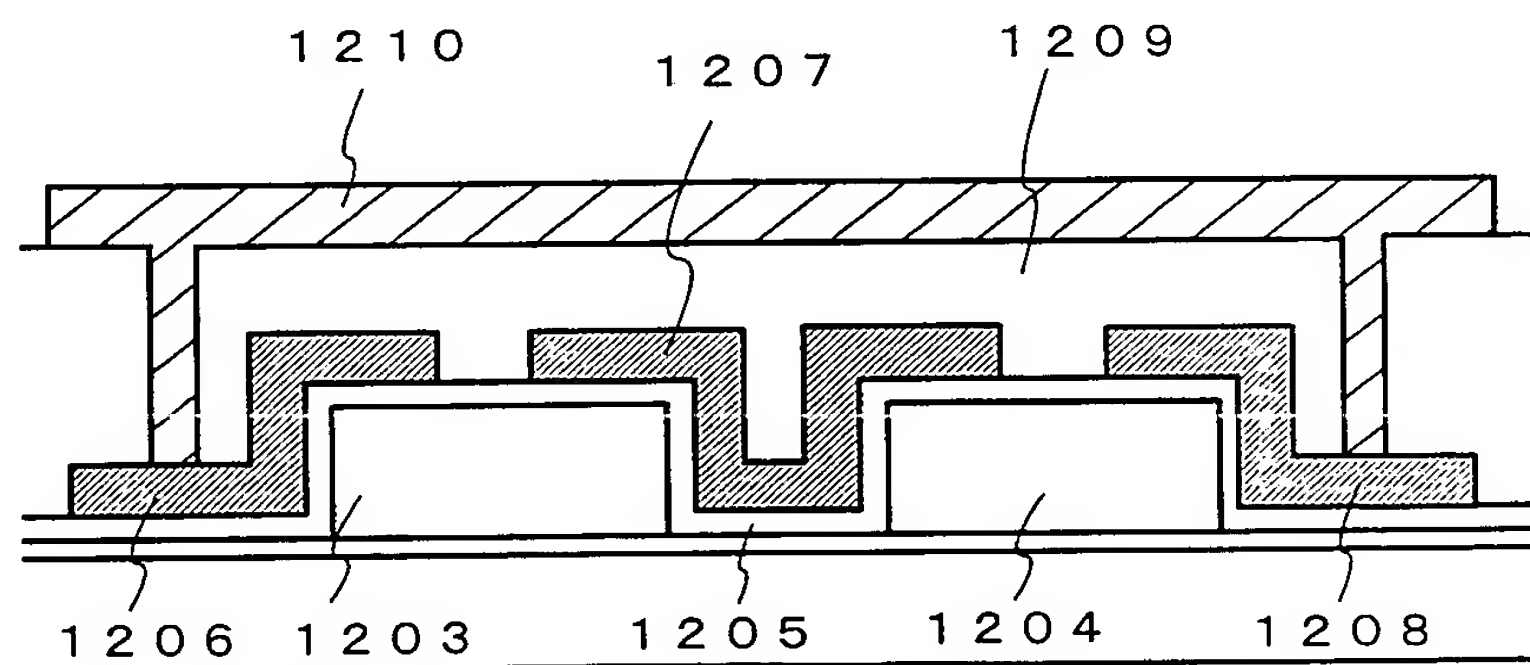


701: DRIVING CIRCUIT REGION

1202: FIRST PROTECTIVE CIRCUIT

703: PIXEL REGION

FIG. 13A



E ——— E'

FIG. 13B

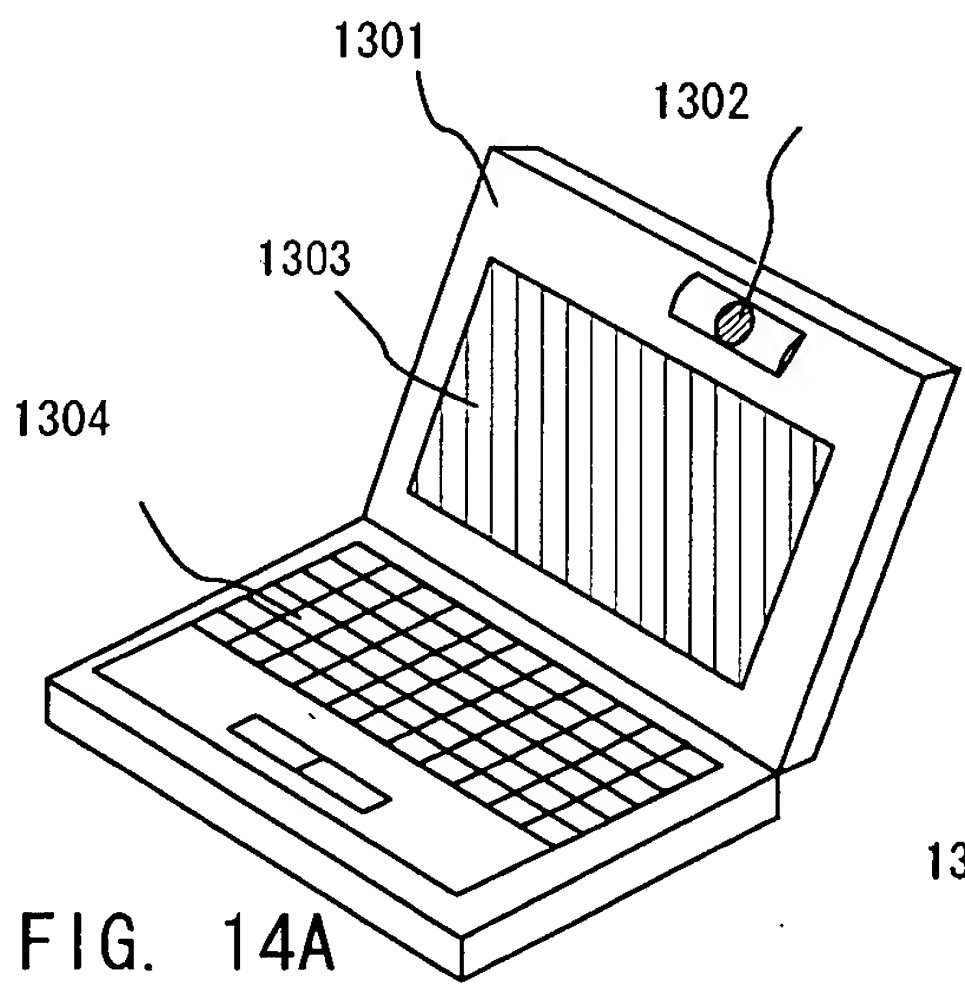


FIG. 14A

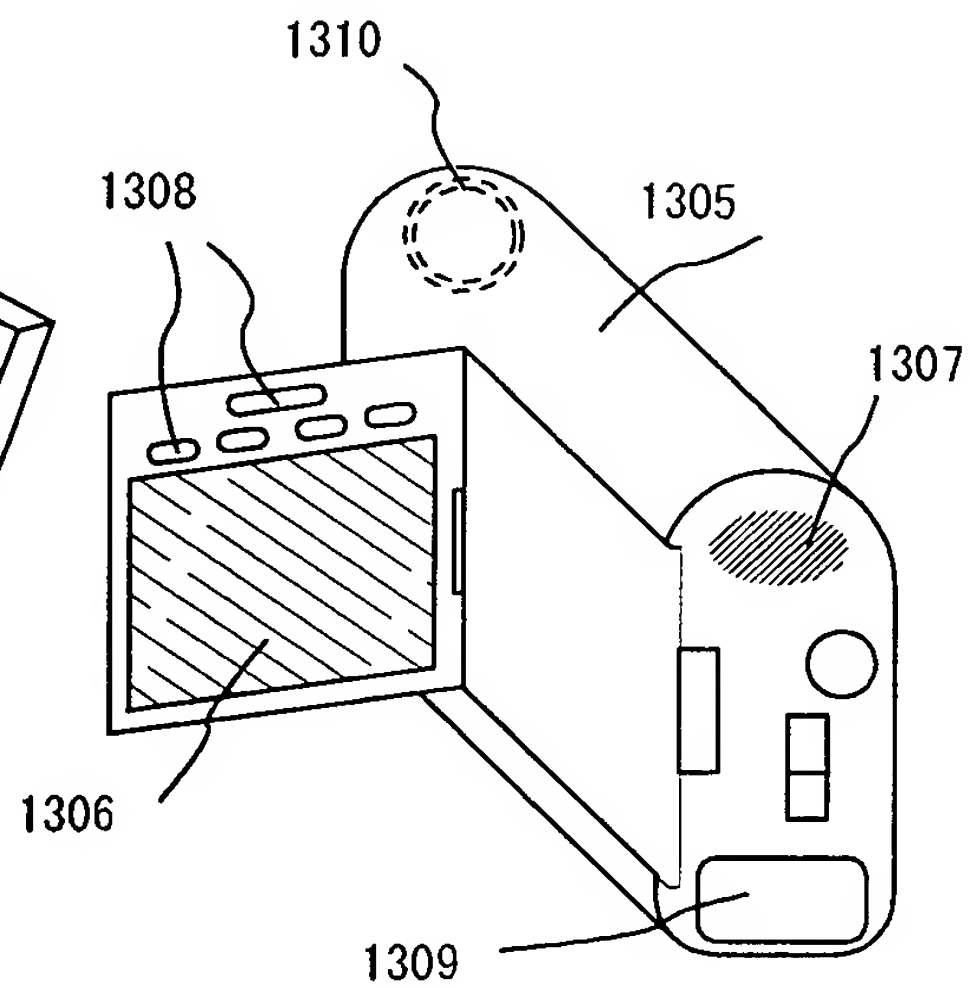


FIG. 14B

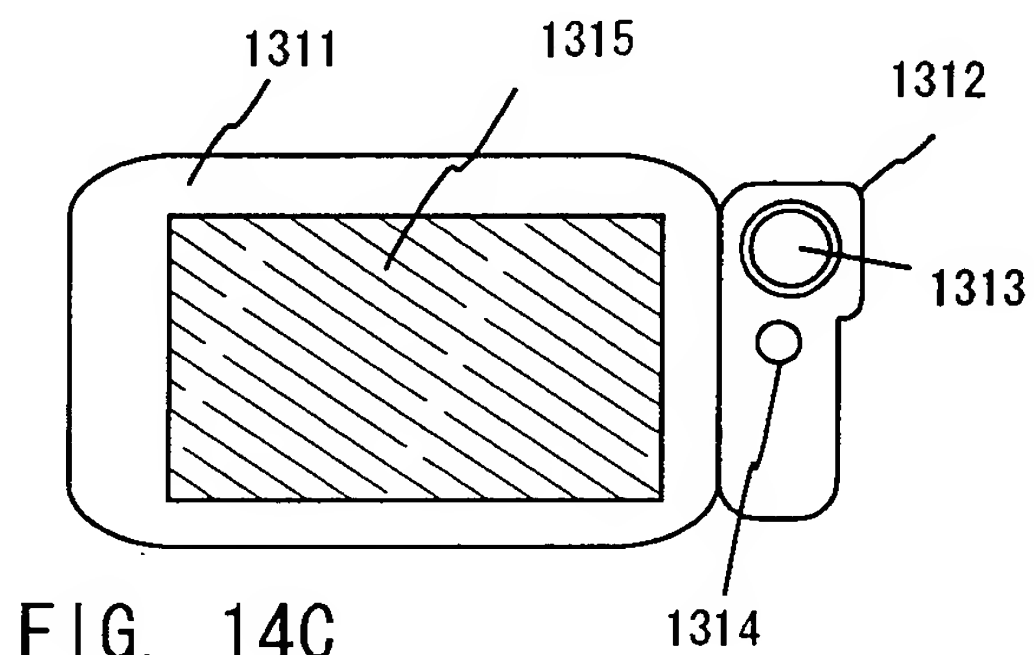


FIG. 14C

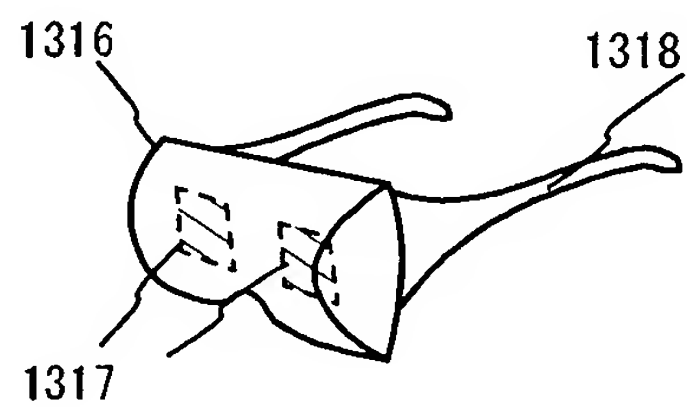


FIG. 14D

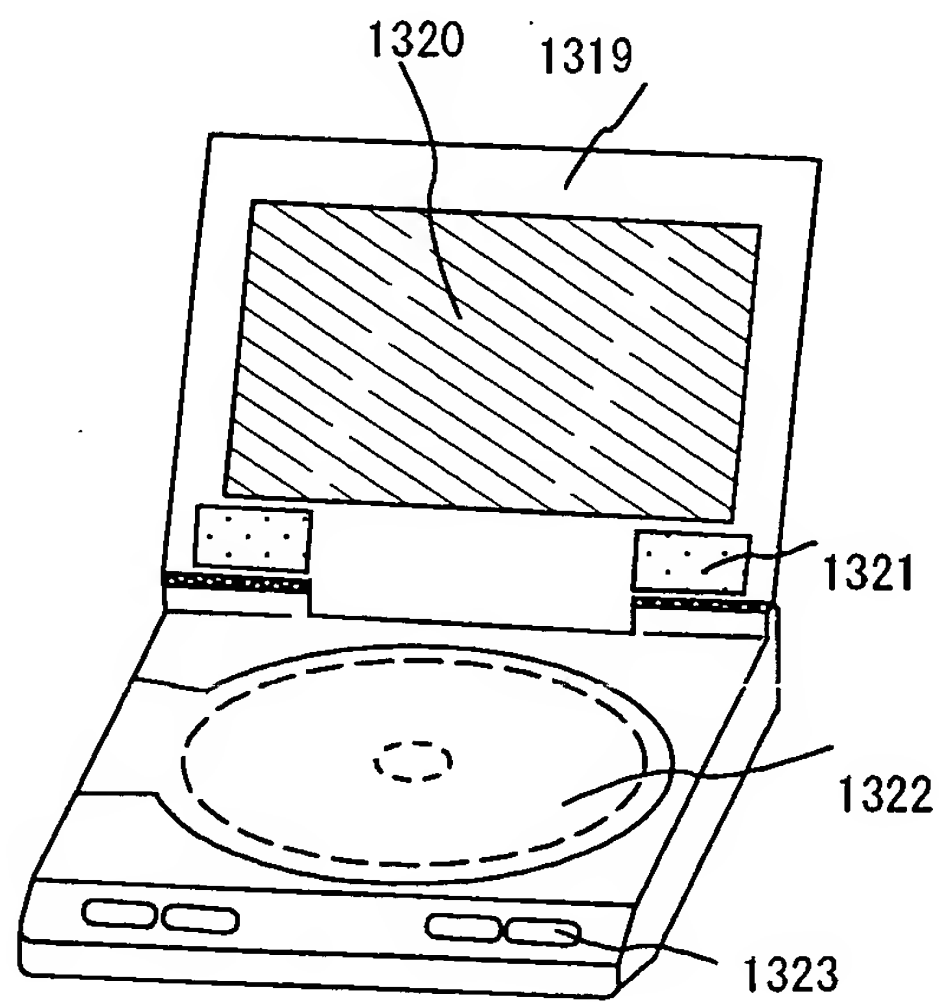


FIG. 14E

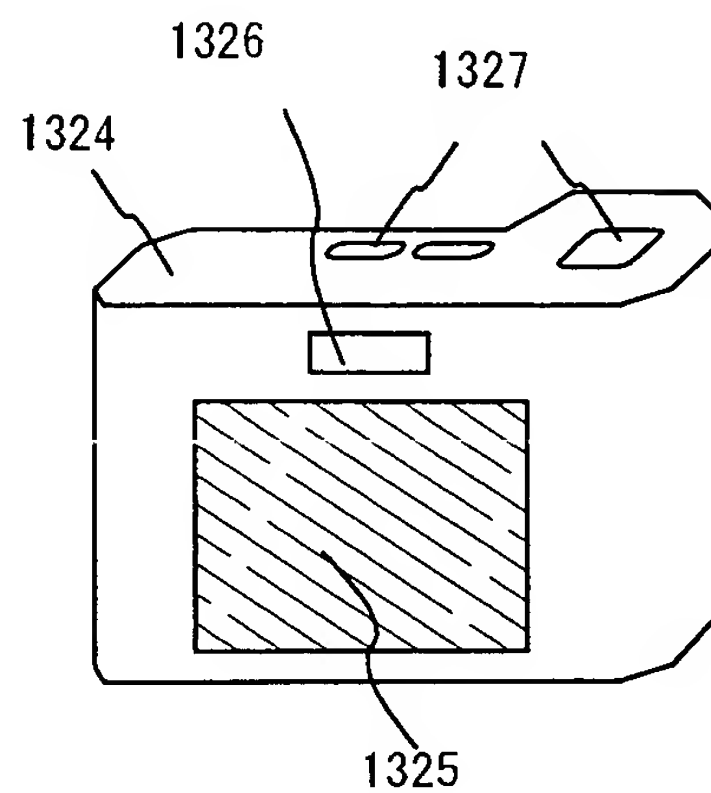


FIG. 14F

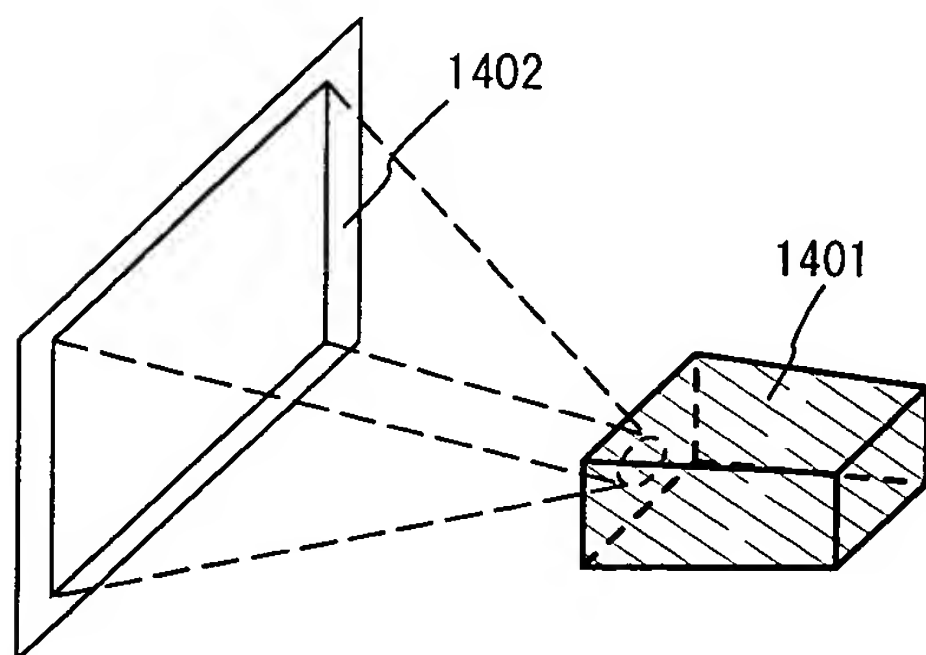


FIG. 15A

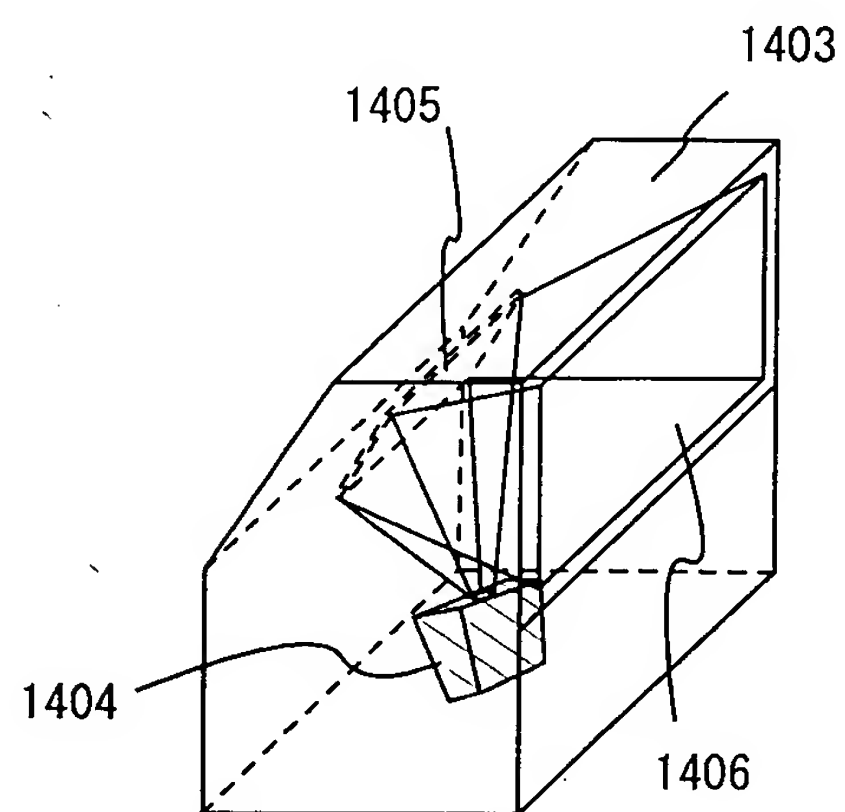


FIG. 15B

1270 11 0
1271 11 0
1272 11 0
1273 11 0
1274 11 0
1275 11 0
1276 11 0
1277 11 0
1278 11 0
1279 11 0
1280 11 0
1281 11 0
1282 11 0
1283 11 0
1284 11 0
1285 11 0
1286 11 0
1287 11 0
1288 11 0
1289 11 0
1290 11 0

FIG. 16A



FIG. 16B

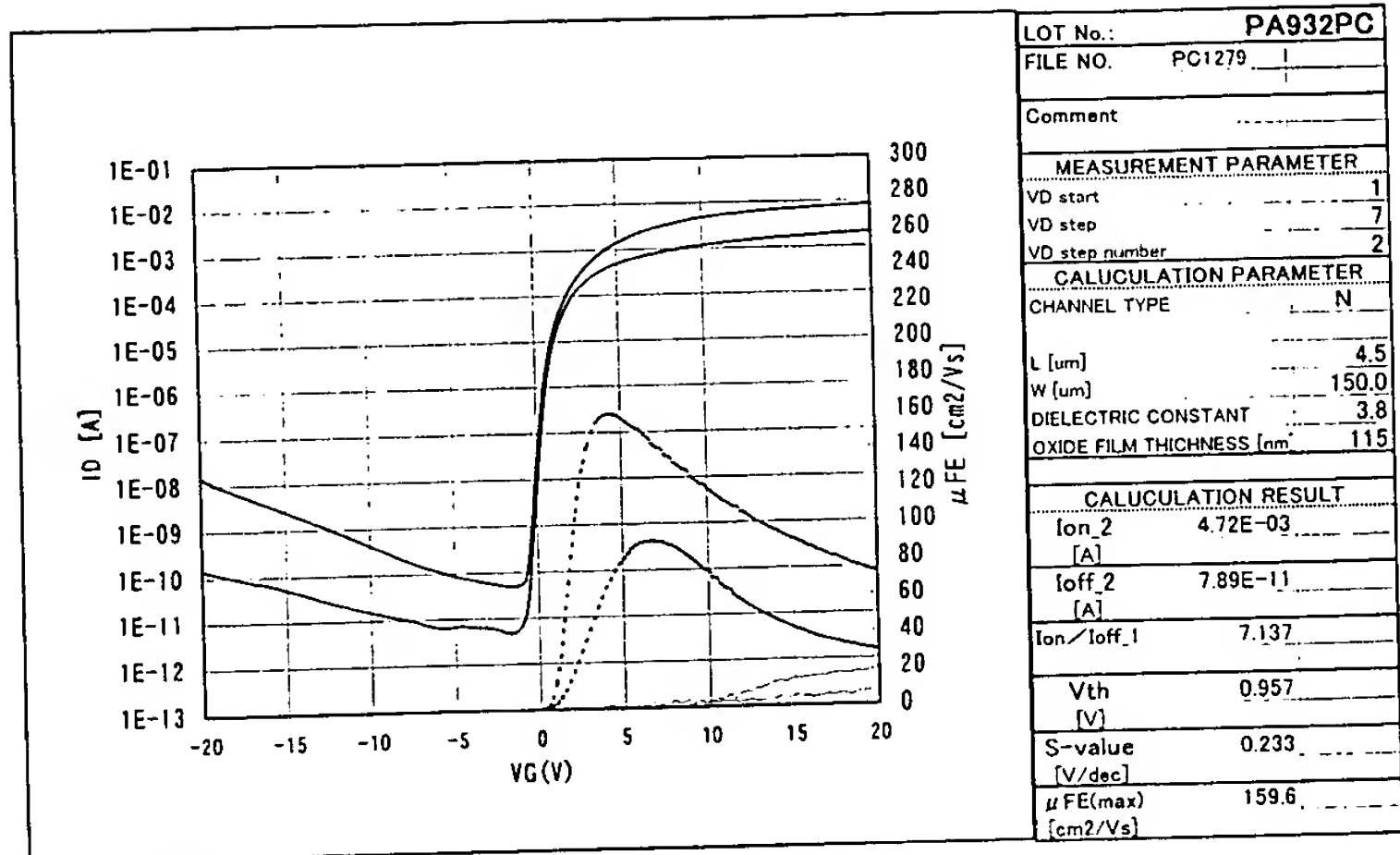


FIG. 17A

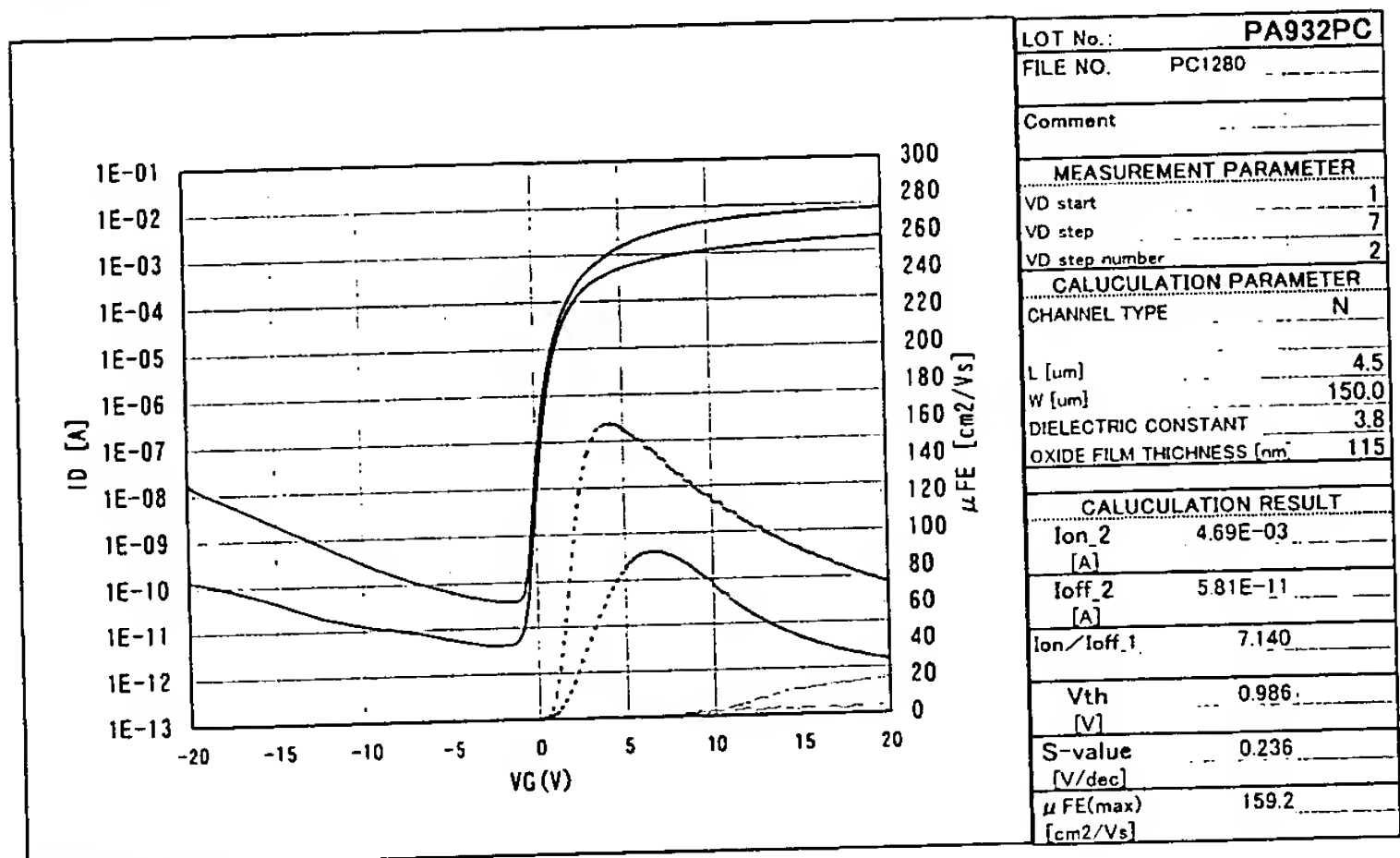


FIG. 17B

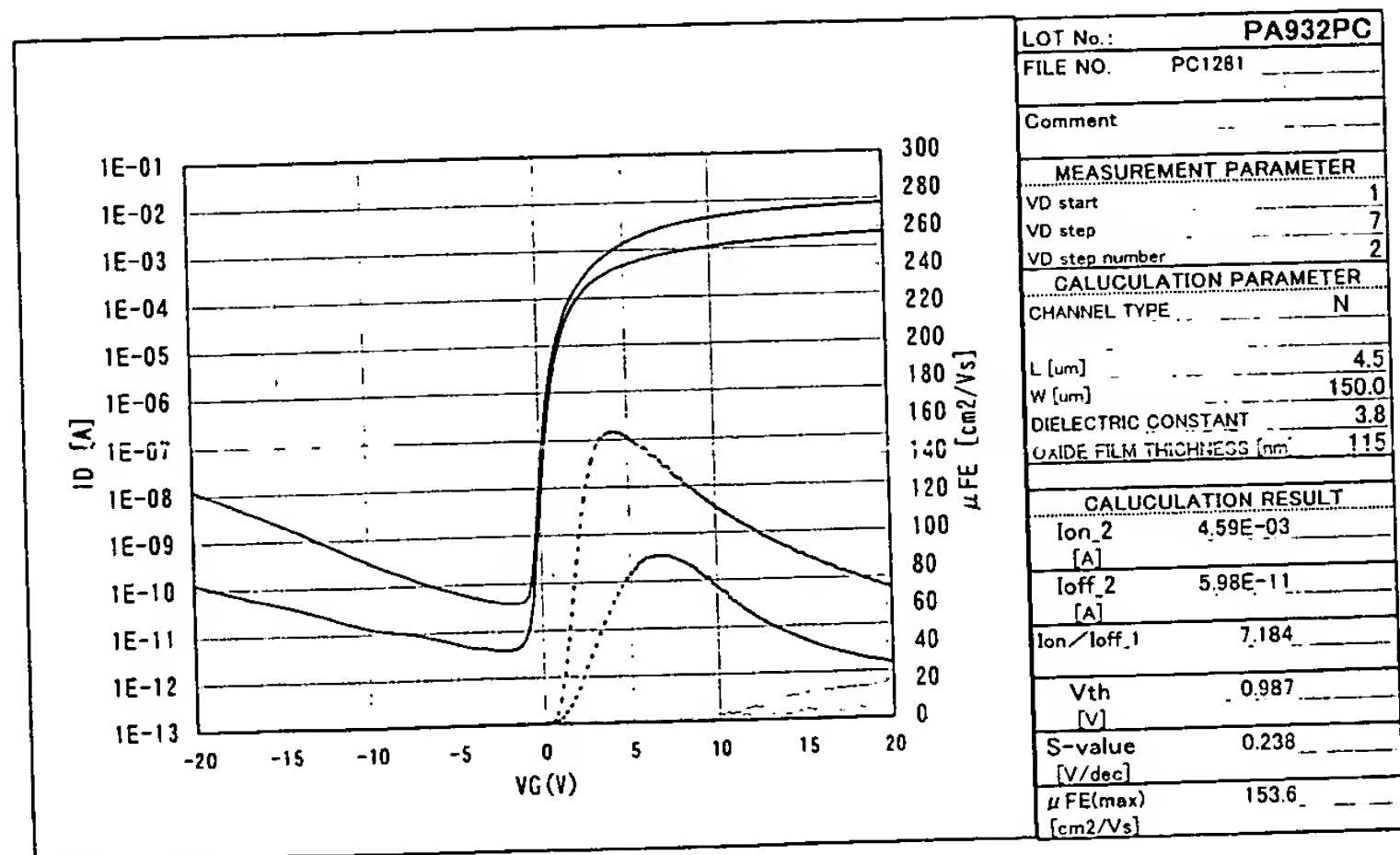


FIG. 17C

1005043 120404

Pixel Region

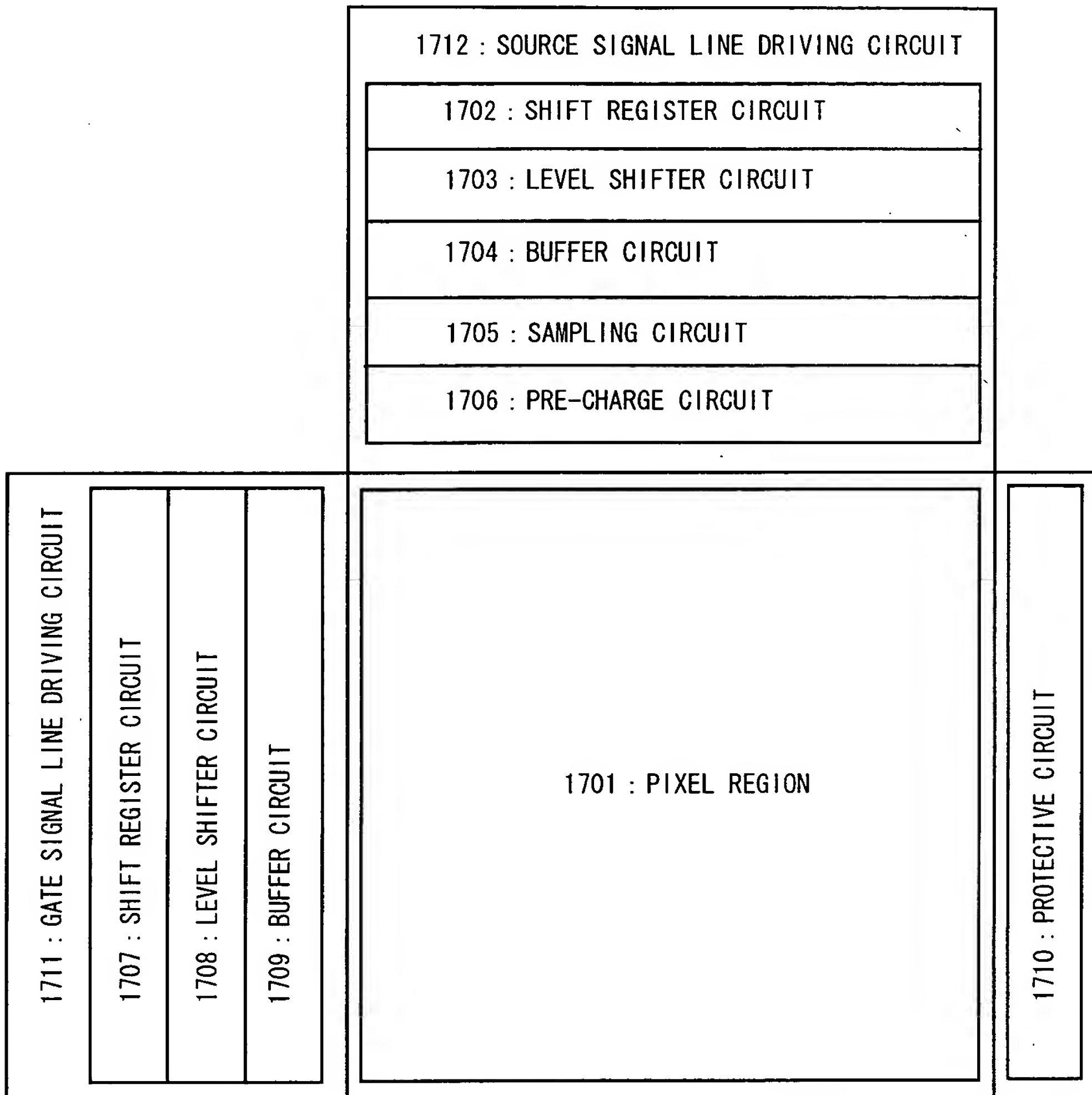


FIG. 18

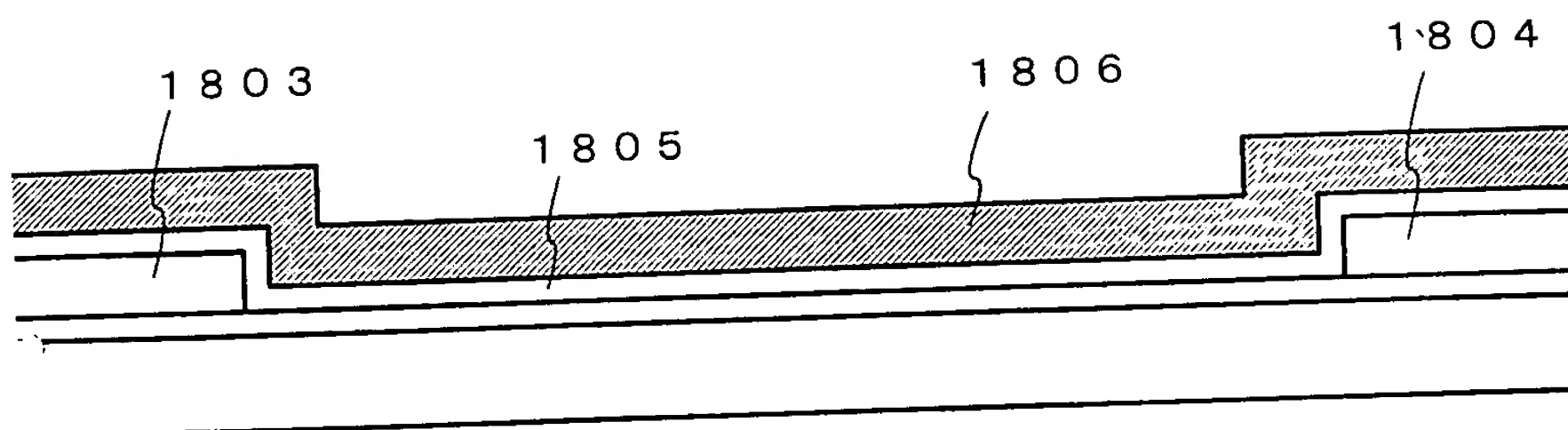


FIG. 19A

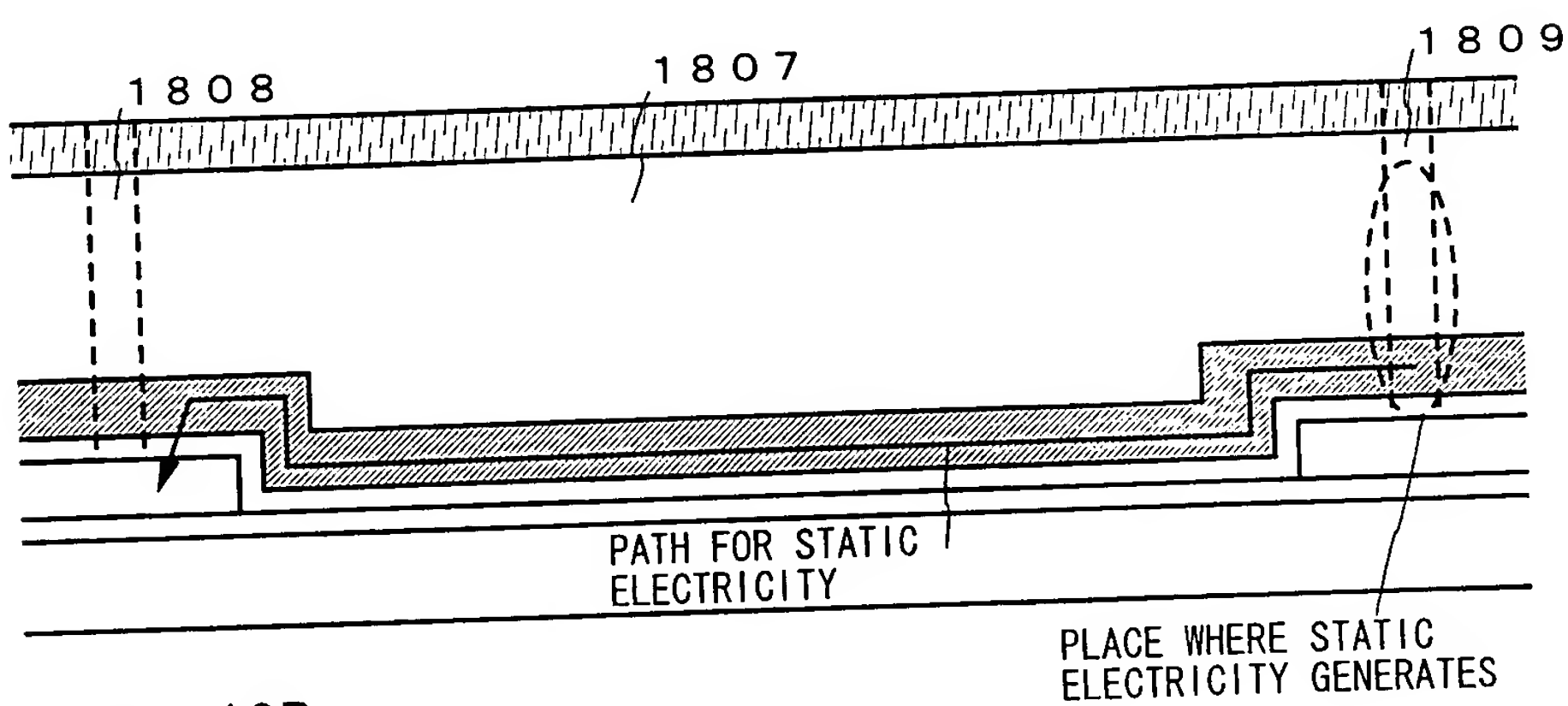


FIG. 19B

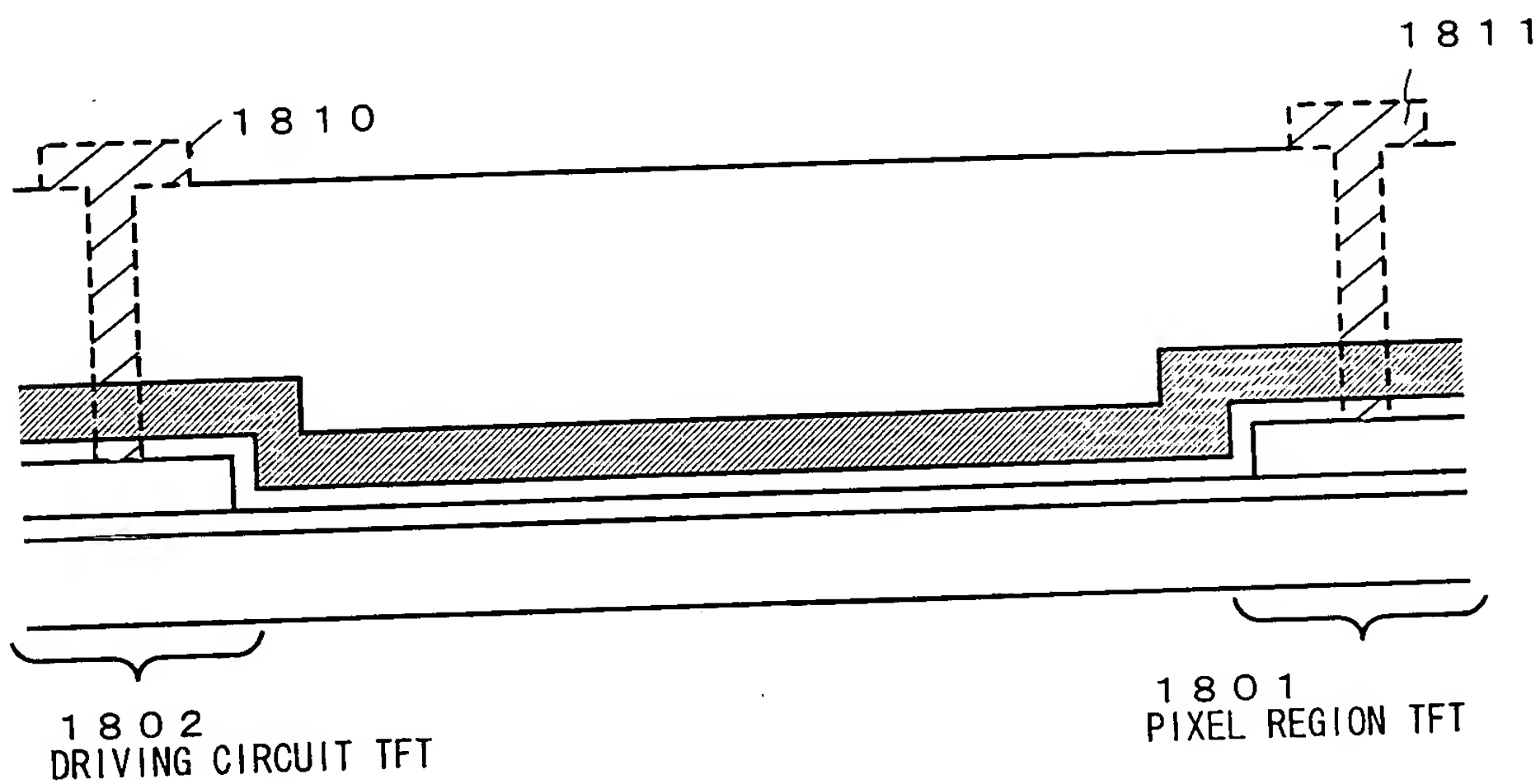


FIG. 19C

FIG. 20

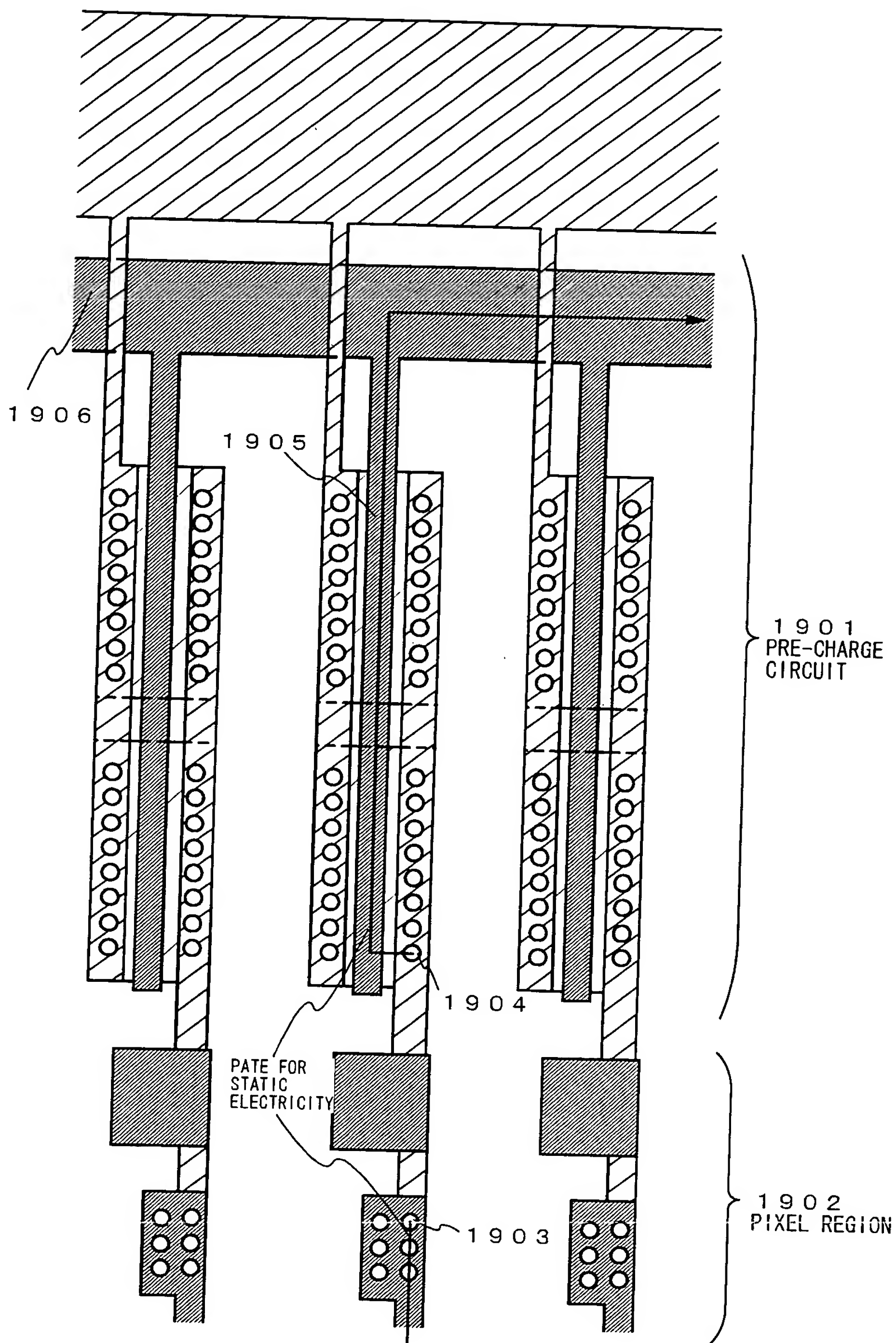


FIG. 20